

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10530883	
	Filing Date		2005-04-11	
	First Named Inventor	Sugawara		
	Art Unit	2826		
	Examiner Name	Sefer, Ahmed N.		
Attorney Docket Number		3688KE-1		

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1	STAHLBUSH, FEDISON, ARTHUR, ROWLAND, KRETCHMER AND WANG, "Propagation of Current-Induced Stacking Faults and Forward Voltage Degradation in 4H-SiC PIN Diodes", Materials Science Forum, Vols. 389-393 pp. 427-430, 2002 Trans Tech Publications, Switzerland.	<input type="checkbox"/>
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- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☒ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Brian J. Ignat/	Date (YYYY-MM-DD)	2007-08-27
Name/Print	Brian J. Ignat	Registration Number	57,174

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